



Symposium: New Frontiers in Memory Technology

September 18, 2007

Industry Leaders to Discuss Critical Issues for Future Flash, DRAM Scaling

SANTA CLARA, Calif.--(BUSINESS WIRE)--Sept. 18, 2007--IEEE SCV-EDS(1) and Applied Materials will host a full-day technical symposium to discuss the critical innovations that will be required to scale memory technology for the next decade. Industry experts from the world's leading semiconductor companies and universities will share their insights regarding this timely topic. The symposium will be held on September 20, 2007 in San Jose, Calif.

"Memory technology is now the major force behind the semiconductor industry, driven by the exponential growth of consumer applications," said Philippe Jansen, Chairman, IEEE SCV-EDS. "But there are still major hurdles to overcome, including the need to re-engineer Flash and DRAM cell structures to extend scaling through the next decade. This event provides an opportunity for attendees to hear about these innovations from leading technologists and researchers, while also providing an opportunity for discussion and sharing of insights."

Presenters: Freescale, Hynix, IBM, IMEC, Intel, Ovonyx, SanDisk, Stanford, Toshiba and Applied Materials

For registration and other program information, visit the Applied Materials home page at http://www.appliedmaterials.com/2007_Memory_Symposium.

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(1) IEEE Santa Clara Valley-Electron Devices Society

CONTACT: Applied Materials, Inc.
Connie Duncan (technical media), 408-563-6209
Patricia Zepeda (business media), 408-563-8160

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